

SD830 - SD860

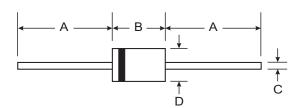
8.0A SCHOTTKY BARRIER RECTIFIERS

Features

- High Current Capability and Low Forward Drop
- **High Surge Capacity**
- **Guard Ring for Transient Protection**
- Low Power Loss, High Efficiency
- Lead Free Finish, RoHS Compliant (Note 3)

Mechanical Data

- Case: DO-201AD
- Case Material: Molded Plastic. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020C
- Terminals: Finish Bright Tin. Solderable per MIL-STD-202, Method 208
- Polarity: Cathode band
- Mounting Position: Any
- Weight: 1.1 grams (approximate)



DO-201AD						
Dim	Min Max					
Α	25.40	_				
В	7.20	9.50				
С	1.20	1.30				
D	4.80	5.30				
All Dimensions in mm						

Maximum Ratings and Electrical Characteristics

@ T_A = 25°C unless otherwise specified

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Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

Characteristic		Symbol	SD830	SD840	SD845	SD860	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage		V _{RRM} V _{RWM} V _R	30	40	45	60	٧
RMS Reverse Voltage		V _{R(RMS)}	21	28	31.5	42	V
Maximum Average Forward Rectified Current	$T_L = 90^{\circ}C$	Io	8.0				Α
Peak Forward Surge current 8.3ms half sine-wave superimposed on rated load (JEDEC Method)		I _{FSM}	175			А	
Maximum Forward Voltage at 8.0A		V _F		0.55		0.70	V
	T _A = 25°C T _A = 100°C	I _R	1.0 50				mA
Typical Thermal Resistance (Note 1)		$R_{\theta JL}$		3	0		K/W
Typical Junction Capacitance (Note 2)		Cj		55	50		pF
Operating and Storage Temperature Range		T _{j,} T _{STG}		-65 to	+150		°C

- Notes: 1. Thermal Resistance from Junction to Lead Vertical PC Board Mounting, 9.5mm Lead Length.
 - 2. Measured at 1.0MHz and applied reverse voltage of 4.0V.
 - 3. RoHS revision 13.2.2003. Glass and High Temperature Solder Exemptions Applied, see EU Directive Annex Notes 5 and 7.



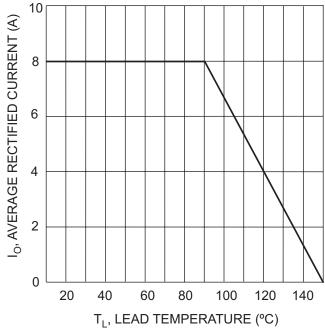


Fig. 1 Forward Current Derating Curve

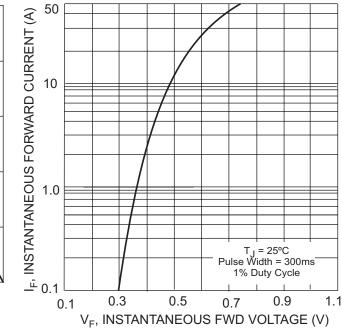
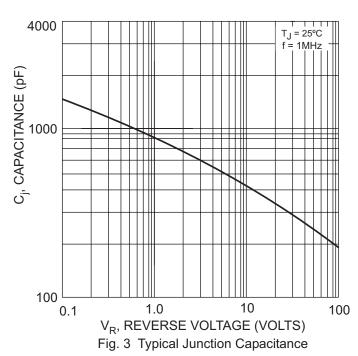


Fig. 2 Typical Forward Characteristics



FSM, PEAK FOWRARD SURGE CURRENT (A) 150 125 100 75 50 8.3 ms Single Half Sine-Wave 25 JEDEC Method 0 1.0 10 100

NUMBER OF CYCLES AT 60Hz Fig. 4 Max Non-Repetitive Peak Fwd Surge Current

IMPORTANT NOTICE

200

175

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